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#86 (1571075630): Design and Analysis of Speed Latency and Power Consumption for Pre-Charged Module Integrated 6T-SRAM Cell for AI-Enabled Circuits

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Paper title *Design and Analysis of Speed Latency and Power Consumption for Pre-Charged Module Integrated 6T-SRAM Cell for AI-Enabled Circuits* [✎](#)

Conference and track [Interdisciplinary Research in Technology & Management 2024](#) - [Track #3: Mechatronics](#) [📄](#)

Abstract

 In this work, we studied the reading and writing speed latency of a unique Pre-Charged Module...

Keywords

CMOS Technology; 6T-SRAM; Pre-charge Module; PDP; SNM; AI 

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Proceedings

The paper will appear neither in the attendee proceedings nor the digital library proceedings since the paper has not been assigned to session. 

Review manuscript	Final manuscript
   no upload: more than 180 days past deadline Nov 15, 2024	  no upload: more than 180 days past deadline Dec 15, 2024